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<b>TRANSMITTAL FORM</b>  <i>(to be used for all correspondence after initial filing)</i>	<b>Application Number</b>	09/739,582	
	<b>Filing Date</b>	12/15/2000	
	<b>First Named Inventor</b>	Steven Teig	
	<b>Group Art Unit</b>	Chris Chu	
	<b>Examiner Name</b>	2815	
<b>Total Number of Pages in This Submission</b>	*	<b>Attorney Docket Number</b>	SPLX.P0005

Enclosures (Check all that apply)		
<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment/Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declarations(s) <input type="checkbox"/> Preliminary Amendment <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input type="checkbox"/> Request to Rescind Previous Non-Publication Request <input checked="" type="checkbox"/> Information Disclosure Statement and PTO-1449 Certified Copy of Priority Documents <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Assignment Papers (for an application) <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application  <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter  <input checked="" type="checkbox"/> Other Enclosures (Listed below): <b>Postcard</b> <b>11 References</b> <b>Copy of eIDS with a Fee Transmittal</b>
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<b>SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT</b>		
<b>Firm Or Individual name</b>	Mani Adeli, Reg. No. 39,585 of Stattler Johansen & Adeli LLP	
<b>Signature</b>		
<b>Date</b>	July 22, 2004	

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PATENT  
Docket No. SPLX.P0005

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Mani Adelf

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In the application of:

Steven Teig

Serial No.: 09/739,582

Filing Date: 12/15/2000

For: INTEGRATED CIRCUIT WIRING  
ARCHITECTURES TO SUPPORT  
INDEPENDENT DESIGNS

Examiner: Chris Chu

Group Art Unit: 2815

**INFORMATION DISCLOSURE  
STATEMENT UNDER 37 C.F.R. § 1.97**

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P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. § 1.97 and § 1.98, applicants submit with this Information Disclosure Statement the attached Form PTO-1449, and copies of the documents listed in the 1449 form for consideration by the Examiner. The Examiner is requested to make these documents of record. Applicants would appreciate the Examiner initialing and returning the Form PTO-1449, indicating that the information has been considered and made of record herein.

This Information Disclosure Statement under 37 C.F.R. § 1.97 is not to be construed as a representation that: (i) a complete search has been made; (ii) additional information material to

the examination of this application does not exist; (iii) the information, protocols, results and the like reported by third parties are accurate or enabling; or (iv) the above information constitutes prior art to the subject invention.

This Information Disclosure Statement is submitted after receipt of a first Office Action on the merits but before the Final Office Action. Accordingly, a fee is required. However, Applicants note that on this day, July 22, 2004, Applicants have submitted another Information Disclosure Statement in electronic form and paid the requisite fee along with this electronic submission. See attached hard copy of electronic IDS and its fee transmittal. Therefore, Applicants submit that no separate fee is required for this Information Disclosure Statement.

However, in the unlikely event that the Patent Office determines that additional fees, extension and/or other relief is required, applicant petitions for any required relief including extensions of time and authorizes the Assistant Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to Deposit Account No. 50-1128 referencing SPLX.P0005. However, the Assistant Commissioner is not authorized to charge the cost of the issue fee to the Deposit Account.

Dated: July 22, 2004

Respectfully submitted,

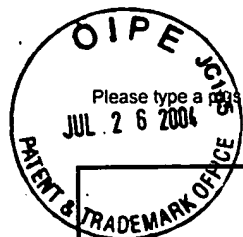
By:

  
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Registration No. 39,585

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<b>Substitute for form 1449A/PTO</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)				<b>Application Number</b>	09/739,582
				<b>Filing Date</b>	12/15/2000
				<b>First Named Inventor</b>	Steven Teig
				<b>Group Art Unit</b>	2815
				<b>Examiner Name</b>	Chris Chu
<b>Sheet</b>	1	of	2	<b>Attorney Docket Number</b>	SPLX.P0005

FOREIGN PATENT DOCUMENTS								
Examiner* Initials	Cite No. <sup>1</sup>	Foreign Patent Document			Date of Publication MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code (if known) <sup>5</sup>				
	1.	JP	2000-082743		03-21-2000	Igarashi et al.	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓

NON PATENT LITERATURE DOCUMENTS					
Examiner* Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T <sup>6</sup>
	2.	Chen et al., Optimal Algorithms for Bubble Sort Based Non-Manhattan Channel Routing, May 1994, Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions Volume: 13 Issues, pp. 603-609.			
	3.	Chen, H. et al., Physical Planning of On-Chip Interconnect Architectures, 2002, IEEE, International Conference, pp. 30-35			
	4.	Cong J. et al., DUNE – A Multilayer Gridless Routing System, May 2001, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, vol 20, iss. 5, pp. 633-647.			
	5.	Dion J. et al., Contour: A Tile-based Gridless Router, March 1995, Digital Western Research Laboratory, research Report 95/3, pp. 1-22			
	6.	Merriam-Webster's Collegiate Dictionary, 10 <sup>th</sup> edition, Merriam-Webster Incorporated, p.606			
	7.	Morton, P. B. et al., An Efficient Sequential Quadratic Programming Formulation of Optimal Wire Spacing for Cross-Talk Noise Avoidance Routing, UCSC-CRL-99-05, March 10, 1999.			
	8.	NN71091316, Use of Relatively Diagonal And Rectangular Wiring Planes n Multilayer Packages, September 1971, IBM Technical Disclosure Bulletin, Vol. No. 14, Issue No. 4, pp. 1316-1317.			

<b>Examiner Signature</b>		<b>Date Considered</b>	
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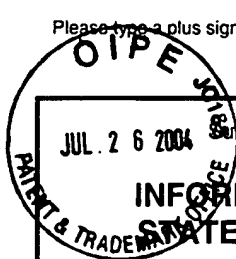
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				Filing Date	12/15/2000
				First Named Inventor	Steven Teig
				Group Art Unit	2815
				Examiner Name	Chris Chu
Sheet	2	of	2	Attorney Docket Number	SPLX.P0005

NON PATENT LITERATURE DOCUMENTS			
Examiner* Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	9.	Royle, J. et al., Geometric Compaction in One Dimension for Channel Routing, 24 <sup>th</sup> ACM/IEEE Design Automation Conference, 1987, pp 140-145.	
	10.	Schiele, W. et al., A Gridless Router for Industrial Design Rule, 27 <sup>th</sup> ACM-IEEE Design Automation Conference, pp. 626-631, 1990.	
	11.	Tseng H. et al., A Gridless Multilayer Router for Standard Cell Circuits Using CTM Cells, Oct. 1999, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, vol 18, iss. 10, pp. 1462-1479.	

Examiner Signature		Date Considered	
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**ELECTRONIC INFORMATION DISCLOSURE STATEMENT**

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	Integrated circuit wiring architectures to support independent designs																																																																																
<p>Application Number: 09/739582 *09/739582*</p> <p>Confirmation Number: 3530</p> <p>First Named Applicant: Steven Teig</p> <p>Attorney Docket Number: SPLX.P0005</p> <p>Art Unit: 2815</p> <p>Examiner: Chris C. Chu</p> <p>Search string: ( 5637920 or 5640327 or 5646830 or 5650653 or 5689433 or 5784289 or 5801385 or 5822214 or 5880969 or 5889329 or 6111756 or 6150193 or 6260183 or 6301686 or 6307256 or 6316838 or 6324674 or 6412097 or 6516455 or 6645842 or 20010009031 or 20020069397 or 20030025205 ),pn.</p> <p><b>US Patent Documents</b></p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td></td><td>1</td><td>5637920</td><td>1997-06-10</td><td>Loo</td><td></td><td></td><td></td></tr><tr><td></td><td>2</td><td>5640327</td><td>1997-06-17</td><td>Ting</td><td></td><td></td><td></td></tr><tr><td></td><td>3</td><td>5646830</td><td>1997-07-08</td><td>Nagano</td><td></td><td></td><td></td></tr><tr><td></td><td>4</td><td>5650653</td><td>1997-07-22</td><td>Rostoker et al.</td><td></td><td></td><td></td></tr><tr><td></td><td>5</td><td>5689433</td><td>1997-11-18</td><td>Edwards</td><td></td><td></td><td></td></tr><tr><td></td><td>6</td><td>5784289</td><td>1998-07-21</td><td>Wang</td><td></td><td></td><td></td></tr><tr><td></td><td>7</td><td>5801385</td><td>1998-09-01</td><td>Endo et al.</td><td></td><td></td><td></td></tr><tr><td></td><td>8</td><td>5822214</td><td>1998-10-13</td><td>Rostoker et al.</td><td></td><td></td><td></td></tr><tr><td></td><td>9</td><td>5880969</td><td>1999-03-09</td><td>Hama et al.</td><td></td><td></td><td></td></tr></tbody></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass		1	5637920	1997-06-10	Loo					2	5640327	1997-06-17	Ting					3	5646830	1997-07-08	Nagano					4	5650653	1997-07-22	Rostoker et al.					5	5689433	1997-11-18	Edwards					6	5784289	1998-07-21	Wang					7	5801385	1998-09-01	Endo et al.					8	5822214	1998-10-13	Rostoker et al.					9	5880969	1999-03-09	Hama et al.			
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	10	5889329	1999-03-30	Rostoker et al.	
	11	6111756	2000-08-29	Moresco	
	12	6150193	2000-11-21	Glenn	
	13	6260183	2001-07-10	Raspopovic et al.	B1
	14	6301686	2001-10-09	Kikuchi et al.	B1
	15	6307256	2001-10-23	Chiang et al.	B1
	16	6316838	2001-11-13	Ozawa et al.	B1
	17	6324674	2001-11-27	Andreev et al.	B2
	18	6412097	2002-06-25	Kikuchi et al.	B1
	19	6516455	2003-02-04	Teig et al.	B1
	20	6645842	2003-11-11	Igarashi et al.	B2

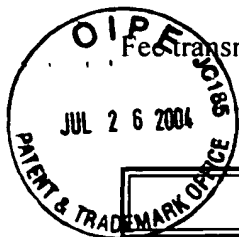
### US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20010009031	2001-07-19	Nitta et al.	A1		
	2	20020069397	2002-06-06	Teig et al.	A1		
	3	20030025205	2003-02-06	Shively	A1		

### Signature

Examiner Name	Date



## FEE TRANSMITTAL

Electronic Version v08

Stylesheet Version v08.0

<b>Title of Invention</b>	Integrated circuit wiring architectures to support independent designs										
Application Number: 09/739582 *09/739582*											
Date: 2000-12-15											
First Named Applicant: Steven Teig											
Attorney Docket Number: SPLX.P0005											
Art Unit: 2815											
Examiner: Chris C. Chu											
<b>TOTAL FEE AUTHORIZED \$180</b>											
Patent fees are subject to annual revisions on or about October 1st of each year.											
<b>BASIC FILING FEE</b>											
<table border="1"><thead><tr><th>Fee Description</th><th>Fee Code</th><th>Amount \$</th><th>Fee Paid \$</th></tr></thead><tbody><tr><td>Submission Of Information Disclosure Stmt Fee</td><td>1806</td><td>180</td><td>180</td></tr></tbody></table>				Fee Description	Fee Code	Amount \$	Fee Paid \$	Submission Of Information Disclosure Stmt Fee	1806	180	180
Fee Description	Fee Code	Amount \$	Fee Paid \$								
Submission Of Information Disclosure Stmt Fee	1806	180	180								
<b>AUTHORIZED BILLING INFORMATION</b>											
The commissioner is hereby authorized to charge indicated fees and credit any overpayments to:											
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Expiration Date (YYYYMMDD): 2004-08-31											
Authorized name: Mani Adeli											
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